


**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: K. ITOH, et al.  
Application No.: 10/796,023  
Filed: March 10, 2004  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH  
IMPROVED STORAGE MOSFET ARRANGEMENT  
Art Unit: 2818  
Examiner: G. N. Auduong

**PETITION FOR EXTENSION OF TIME**

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 29, 2004

Sir:

In the matter of the above-identified application, Applicants hereby respectfully petition for an extension of time to permit filing a response within a one-month time period subsequent to expiration of the shortened statutory period set in the outstanding Office Action mailed June 29, 2004. A credit card payment form in the amount of \$110.00 to cover the required fee for the requested extension of time is attached hereto.

It is respectfully requested that any shortage in the fee be charged to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Docket No. 520.40847CC2) and any excess fees credited to that account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT &amp; KRAUS, LLP

By Gregory E. Montone  
Reg. No. 28,141

GEM/dlt

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